

# 1 The instructions for the using the macro eltex 2.0

This is the version 2.0 of the macro `eltex` for the drawing the circuit diagrams in L<sup>A</sup>T<sub>E</sub>X. Here is the several differences on the comparison with the previous version. In the version 2.0 are the circuit symbols accordingly IEC 617-1 to 11. Here is the helpful grid for the easier placing of the circuit symbols and also the commands for the circuit symbols are different then in the previous version. Old and new version are not compatible. The reason for it is that in the praxis the commands had many parameters and the labels with the fixed coordinates mostly had to be placed in the different place in the drawing. The new macro contains some frequently used symbols "two times" with the shorter and longer leads. This is time saving when you draw the circuit diagram. When you have only small area there is also the symbol with shorter lead. The circuit symbols were created on the actual use for the making the education materials in the communication technics, circuit theory and electronics area. The size of macro is 100kB. Not to occupy many RAM during the compilation, the circuit symbols are distributed into several files `eltex1 ... eltex7` accordingly the area of interest. So you can use only several files and not all files.

This principle of the drawing the circuit diagrams is not comfortable, it would be better to create some interactive graphical editor with the saving the (`eltex`) commands to the file.

## 2 The description of the commands

### 2.1 Basic

This circuit symbols are contained in macro `\eltex1`

`\grid{x}{y}` The command creates the grid with the step 10mm. x - width of the picture in cm, y - height of the picture in cm, only natural number.

#### 2.1.1 Sources

`\hsourcev` Ideal voltage source horizontally oriented.

`\hhsourcev` Ideal voltage source with reduced leads horizontally oriented.

`\vsourcesv` Ideal voltage source vertically oriented.

`\vvsourcev` Ideal voltage source with reduced leads vertically oriented.

`\dsourcesv{x}` Ideal voltage source diagonally oriented, x - letter U - up oriented, D - down oriented.

`\hsourcec` Ideal current source horizontally oriented.

`\hhsourcec` Ideal current source with reduced leads horizontally oriented.

`\vsourcesc` Ideal current source vertically oriented.

`\vvsourcec` Ideal current source with reduced leads vertically oriented.

`\dsourcesc{x}` Ideal current source diagonally oriented, x - letter U - up oriented, D - down oriented.

#### 2.1.2 Resistors

`\hrez{n}` Resistor horizontally oriented. Number n determines the type of the resistor. 0 - basic symbol 1 to 6 variable value resistor, 7 - nonlinear dependent resistor,

`\hrez{n}` Resistor with reduced leads horizontally oriented.

`\vrez{n}` Resistor vertically oriented.

`\vvrez{n}` Resistor with reduced leads vertically oriented.

\drez{x}{n} Resistor diagonally oriented x - letter U - up oriented, D - down oriented, n - number - type of the symbol,  
\ddrez{x}{n} Resistor with reduced leads diagonally oriented.

### 2.1.3 Capacitors

\hcap{n} Capacitor horizontally oriented. Number n type of the symbol. 0 - basic symbol 1, 4 variable capacitor  
\hhcap{n} Capacitor with reduced leads horizontally oriented.  
\vcap{n} Capacitor horizontally oriented.  
\vvcap{n} Capacitor with reduced leads vertically oriented.  
\dcap{x}{n} Capacitor diagonally oriented. x letter - U up oriented, D down oriented, n number - type of the symbol.

### 2.1.4 Inductors

\hturn{x}{n} Two turns horizontally oriented, x - letter - U position up oriented, D down, n = 0 basic symbol, n = 1 variable inductor.  
\vturn{x}{n} Two turns vertically oriented, x - letter - L position left oriented, R right, n = 0 basic symbol, n = 1 variable inductor.  
\hind{x}{n} Inductor horizontally oriented, x - letter - U position up, D down, n = 0 basic symbol, n = 1 variable inductor.  
\hind{x}{n} Inductor with reduced leads horizontally oriented.  
\wind{x}{n} Inductor vertically oriented, x - letter - L left position, R right, n = 0 basic symbol, n = 1 variable inductor.  
\vvind{x}{n} Inductor with reduced leads vertically oriented.  
\dind{x}{y}{n} Inductor diagonally oriented, Bezier macro must be selected, x - letter - U symbol up oriented, D down, y - letter - L turns left oriented, R right, n = 0 basic symbol, n = 1 variable inductor.

### 2.1.5 Cores

\hcore{n}{x} Core horizontally oriented, x - letter - core type I iron F ferrite, n - natural number - length of the symbol in mm for (I) case, or member of the dashed lines for (F) case.  
\vcore{n}{x} Core vertically oriented.  
\dcore{x}{n}{y} Core diagonally oriented, x - letter - U symbol up, D symbol down, n - natural number - length of the symbol for (I) case, or number of the dashed lines for (F) case, y - core type I iron F ferrite.  
\simb{n} Symbols of the mutual orientation of winding, n number 1 •, 2 = □, 3 = △.

### 2.1.6 Switches

\hswitch{n} Switch horizontally oriented, n=0 switched on, 1 off.  
\vswitch{n} Switch vertically oriented, n=0 switched on, 1 off.  
\oswitch{x}{n} Overswitch horizontally oriented x - letter - R right, L left, n=0 switched on, 1 off.  
\voswitch{x}{n} Overswitch vertically oriented , x - letter - symbol U up, D down, n=0 switched on, 1 off.

### 2.1.7 Basic symbols

\node connection of the leads.  
\pin terminal  
\hwire{n} wire in horizontal position, n length of the symbol in mm.  
\vwire{n} wire in vertical position, n length of the symbol in mm.  
\dwire{x}{n} wire in the diagonal position, x letter - position U up , D down, n length of the symbol.  
\cloop{x}{y} Symbol for the simple loop (mesh), x letter- R clockwise orientation, L anticlockwise, y - text- label of the loop.  
\earth{x} Earth, x - letter - position D down, U up, L left, R right.  
\chassis{x} Chassis, x - letter - position D down, U up, L left, R right.  
\measure{x} Measuring instrument indicating horizontally oriented, x - text - symbol of the measured quantity.  
\hmeasure{x} Measuring instrument indicating horizontally oriented, reduced leads.  
\vmeasure{x} Measuring instrument indicating vertically oriented,  
\vvmeasure{x} Measuring instrument indicating vertically oriented, reduced leads.  
\osc{x} Oscilloscope, x - letter - I indicating, R recording.

## 2.2 Semiconductors

This circuit symbols are contained in macro \eltex2

\graetz{x} Diode bridge rectifier, x - letter - R plus pole right, L plus pole left.  
\diode{x}{y} Diode horizontally oriented, x - letter - R cathode right, L cathode left, y - letter - C varicap, Z Zener diode, E Esaki diode, S Shottky diode, D LED, P photodiode, L laser diode, blanc letter - basic symbol,  
\diode{x}{y} Diode vertically oriented, x - letter - U cathode up, D cathode down, y - letter - type of the diode.  
\ddiode{x}{y} Diode diagonally oriented, x - letter - U symbol up, D down, y - letter - U cathode up, D down.  
\triac{x} Triac horizontally oriented, x - letter - gate A left down, B left up, C right down, D right up.  
\triac{x} Triac vertically oriented, x - letter - gate A left down, B left up, C right down, D right up.  
\thyristor{x}{y} Thyristor horizontally oriented, x - letter - R cathode right, L left, y - gate A left down, B left up, C right down, D right up.  
\thyristor{x}{y} Thyristor vertically oriented, x - letter - U cathode up, D down, y - gate A left down, B left up, C right down, D right up.  
\thyristor{x}{y}{z} Thyristor diagonally oriented, x - letter - symbol U up, D down, y - U cathode up D down, z -gate A left down, B left up, C right down, D right up.  
\diac Diac horizontally oriented.  
\diac Diac vertically oriented.  
\opto{x} Optocoupler horizontally oriented, x - letter - R transistor right, L left.  
\opamp{x}{y} Operational amplifier, x - letter - output R right, L left, U up, D down, y - inverting input U up, D down, R right, L left.  
\ota{x}{y}{z} Operational transconductance amplifier, x - letter - output R right, L left, U up, D down, y - inverting input U up, D down, R right, L left, z - letter Y OTA with linearising diodes, blanc - without diodes.  
\bota{x}{y}{z} Balanced output transconductance amplifier, (x z y as in OTA)

**\bjt{x}{y}{z}{v}** Bipolar junction transistor, x - type of the conductivity N npn, P pnp, y - collector emitter R right, L left, U up, D down, z - emitter position R right, L left, U up, D down, v - symbol in the circle N no, Y yes.  
**\jfet{x}{y}{z}{v}** JFET, x - letter - channel N or P, y - letter - position drain source R right, L left , U up, D down, z - letter - position source R right, L left, U up, D down, v - letter - circle Y yes, N (or blanc) no.  
**\mos{x}{y}{z}{v}{w}{r}** MOS transistor, x - letter - channel N or P, y - letter - position drain source R right, L left , U up, D down, z - letter - position source R right, L left, U up, D down, v - letter - mode E enhancement, D depletion, w - number of the gates 1 or 2, r - letter - circle Y yes, N (or blanc) no.  
**\ujt{x}{y}{z}{v}** unijunction transistor (twobase diode), x - letter - emitter N or P, y - letter - circuit symbol position B1 B2, R right, L left , U up, D down, z - letter - position B1 - R right, L left, U up, D down, v - letter - circle Y yes, N (or blanc) no.  
**\hall** Hall's generator  
**\hmag** Magnistor horizontally.  
**\vmag** Magnistor vertically.  
**\hptc** Thermistor PTC horizontally.  
**\vptc** Thermistor PTC vertically.  
**\hntc** Thermistor NTC horizontally.  
**\vntc** Thermistor NTC vertically.  
**\hvar** Varistor horizontally.  
**\var** Varistor vertically.  
**\hprez** Photoresistor horizontally.  
**\vprez** Photoresistor vertically.  
**\hpelt** Peltier's cell horizontally.

## 2.3 Special circuits

This circuit symbols are contained in macro \eltex3

**\deltaload** Load in the delta configuration.  
**\starload** Load in the star configuration.  
**\starsource** Sources in the star configuration.  
**\neta** **\netb** **\netc** **\netd** **\nete** Elementary twoports configurations.  
**\inet** I network.  
**\gneta** **\gnedb** Gamma network.  
**\lneta** **\lnetb** L network.  
**\pineta** **\pinetb**  $\pi$  network.  
**\tneta** **\tnetb** T network.  
**\xnet{n}** X network, n - number - 1 general impedances, 2 symmetrical X network.  
**\bhnet** Bridget H network.  
**\hnet** H network.  
**\btnet** Bridget T network.  
**\ttnet** Double T network.  
**\gyrator** Gyrator.  
**\unistor{x}** Unistor, x - letter - orientation R right, L left, U up, D down.  
**\floop{x}** Closed loop in the flowgraphs, x - letter - loop orientation U up, D down.

## 2.4 Electron tubes

This circuit symbols are contained in macro \eltex4

**\diode{x}{y}** Diode, x - letter - filament Y yes, N (or blanc) no, y - letter - equipotential cathode Y yes, N (or blanc) no.  
**\triode{x}{y}** Triode, x - letter - filament Y yes, N (or blanc) no, y - letter - equipotential cathode Y yes, N (or blanc) no.  
**\tetrode{x}{y}** Tetrode, x - letter - filament Y yes, N (or blanc) no, y - letter - equipotential cathode Y yes, N (or blanc) no.  
**\pentode{x}{y}{z}** Pentode, x - letter - filament Y yes, N (or blanc) no, y - letter - equipotential cathode Y yes, N (or blanc) no, z - letter - suppressor grid and cathode connection Y yes, N (or blanc) no.  
**\heptode{x}{y}{z}** Heptode, x - letter - filament Y yes, N (or blanc) no, y - letter - equipotential cathode Y yes, N (or blanc) no, z - letter - cathode and suppressor grid and screen grids g2 and g4 connection Y yes, N (or blanc) no.

## 2.5 Other basic circuit symbols

This circuit symbols are contained in macro \elte5

**\hfuse** Fuse horizontally.  
**\vfuse** Fuse vertically.  
**\hrelay** Relay horizontally.  
**\vrelay** Relay vertically.  
**\hlight** Light source general (bulb) horizontally.  
**\vlight** Light source general (bulb) vertically.  
**\bell** Bell horizontally.  
**\buzzer** Buzzer horizontally.  
**\siren** Siren horizontally.  
**\microphone{x}** Microphone, x - letter - R right oriented, L left.  
**\earphone{x}** Earphone, x - letter - R right oriented, L left.  
**\loudspeaker{x}** Loudspeaker, x - letter - R right oriented, L left.  
**\hcrystal** Quartz crystal horizontally.  
**\vcrystal** Crystal vertically.  
**\hgap** Spark gap horizontally.  
**\vgap** Spark gap vertically.  
**\antenna{n}** Antenna, n - number - 0 transmitting, 1 receiving, 2 transmission and reception alternatively, 3 transmission and reception instantaneously.  
**\dipole{n}** Dipole, n - number - 0 single dipole, 1 folded dipole.  
**\loopant** Loop antenna.  
**\cell{x}{y}** Galvanic cell horizontally or vertically, x - letter - plus pole R right, L left, U up, D down, y - letter - Y photocell, N (or blanc) galvanic cell.  
**\hglow** Glow lamp horizontally.  
**\vglow** Glow lamp vertically.  
**\hdlamp** Discharge lamp horizontally.  
**\vdlamp** Discharge lamp vertically.  
**\hflamp** Fluorescent lamp horizontally.  
**\vflamp** Fluorescent lamp vertically.  
**\sensor{x}** Sensor of the quantity, output in the left. x - text - symbol of the quantity e. g.  $\vartheta$  temperature.  
**\head{n}** converter head, output on the left. n - number - 1 mechanical stereo receiving, 2 magnetic mono receiving, 3 magnetic mono recording, 4 magnetic mono cleaning, 5 magnetic mono combined, 6 optical receiving.

## 2.6 Block symbols

This circuit symbols are contained in macro \eltex6

```
\ptran Transformer
\fgen{n} Wave generator, n - number - 1 sine-wave, 2 square-wave, 3 sine-wave
with variable frequency.
\delay Delay line
\amplifier{x} Amplifier, x - letter - orientation R right, L left.
\filter{n} Frequency filter, n - number - 1 low pass, 2 high pass, 3 band pass, 4
band stop.
\compressor Compressor.
\expander Expander.
\deemphase Filter deemphase.
\preemphase Filter preemphase.
\artline Artificial line.
\converter Converter.
\corrector{n} Corrector, n - number - 1 amplitude distortion corrector, 2 phase
corrector, 3 group delay corrector.
\limiter Limiter.
\balance Balance.
\termin{n} Termination set, n - number - 1 with balancing network, other number
- without balancing network.
\hybrid Hybrid transformer.
\modulator Modulator.
\attenuator Attenuator.
\carrier{n} Carrier frequency, n - number - 1 carrier, 2 partially suppressed carrier,
3 suppressed carrier.
\freq{n} Frequency, n - number - 1 pilot frequency, 2 signalling frequency. 3
measuring frequency.
\pilot{n} Pilot frequency, n - 1 basic group, 2 super group, 3 master group, 4
super master group.
\band{n} Frequency band, n - 1 frequency noninverted, 2 band ad 1 phase inverted,
3 frequency inverted, 4 band ad 3 phase inverted.
\ltran{x}{n} Light transmitter, x - letter - light transmission R right, L left, n -
number - light - 1 coherent, other number - uncoherent.
\lrec{x}{n} Light receiver, x - letter - light reception R right, L left, n - number
- light 1 coherent, other number - uncoherent.
\fibre{x} Optical fibre, x - letter - S single mode step refraction index, M multi
mode step refraction index, blank letter - general optical fibre.
\threephase{x} Three phase source, x - letter - D delta, S star.
\rgraetz Bridge rectifier.
\trafo{x} Single phase transformer, x - letter - position H horizontally, V vertically.
\engine{n} Engine, n - number - 0 direct current, 1 single phase, 3 three phase , 4
linear, 5 stepping.
\gener{n} Generator, n - number - 0 direct current, 1 single phase, 3 three phase.
```

## 2.7 Logical circuit

This circuit symbols are contained in macro \eltex7

```
\andnand{n}{m}{x} Logical gate AND, n - number - number of inputs 2 two inputs,
3 three inputs, m - number - type of the gate 0 basic, 1 power, 2 basic with open
```

collector, 3 power with open collector, x - letter - I inverted output, N noninverted output.

\ornor{n}{m}{x} Logical gate OR, n - number - number of inputs 2 two inputs, 3 three inputs, m - number - type of the gate 0 basic, 1 power, 2 basic with open collector, 3 power with open collector, x - letter - I inverted output, N noninverted output.

\invert{m}{x} Inverter, m - number - type of the gate 0 basic, 1 power, 2 basic with open collector, 3 power with open collector, x - letter - I inverted output, N noninverted output.

### 3 Using of the commands

It is necessary to select the macro `eltex` a `bezier` In the case of "old" L<sup>A</sup>T<sub>E</sub>X2.09 there are the commands:

```
\documentstyle[a4,bezier]{article}
\input{eltex1}
% if you need also \input{eltex2} ..... \input{eltex7}
\begin{document}

\begin{figure}
\begin{center}
\begin{picture}(100,80)(0,0) % picture size 100 x 80 mm

\grid{10}{8} % grid with the step 10 mm
% mesh is numbered in mm
% to make easy the orientation
% After finishing the picture
% you can cancel this command

\put(30,30){\dind{U}{R}{1} }

\end{picture}
\end{center}
\caption{\it Circuit diagram.}
\label{fig:agic1}
\end{figure}

here is any text

\end{document}
```

In the case of L<sup>A</sup>T<sub>E</sub>X2e is used only different heading of the document.

### 4 Example

```
\begin{figure}
\begin{center}
\begin{picture}(100,80)(0,0)
\grid{10}{8}
\put(40,40){\mos{N}{R}{D}{D}{2}{Y}}
\put(10,40){\hcap{O}}
\end{picture}
\end{center}

```

```

\put(40,10){\vrez{0}}
\put(40,40){\vrez{1}}
\put(30,10){\vrez{0}}
\put(30,40){\vrez{0}}
\put(60,10){\vrez{0}}
\put(70,10){\vcap{0}}
\put(60,45){\vvind[R]{0}}
\put(70,50){\vturn[L]{0}}
\put(65,50){\vcore[3]{F}}
\put(60,10){\chassis{D}}
\put(60,65){\vwire{5}}
\put(60,70){\hwire{30}}
\put(60,40){\hwire{10}}
\put(10,10){\hwire{80}}
\put(70,50){\hwire{10}}
\put(70,56){\hwire{10}}
\put(10,70){\hwire{30}}
\put(30,10){\node}
\put(40,10){\node}
\put(60,10){\node}
\put(70,10){\node}
\put(30,40){\node}
\put(40,43.5){\node}
\put(60,40){\node}
\put(30,70){\node}
\put(90.5,10){\pin}
\put(80.5,50){\pin}
\put(80.5,56){\pin}
\put(90.5,70){\pin}
\put(9,10){\pin}
\put(9,70){\pin}
\put(9,40){\pin}
\put(20,45){$C_{\{1\}}$}
\put(20,55){$R_{\{1\}}$}
\put(20,25){$R_{\{2\}}$}
\put(45,62){$R_{\{3\}}$}
\put(45,25){$R_{\{4\}}$}
\put(53,25){$R_{\{5\}}$}
\put(75,25){$C_{\{2\}}$}
\put(90,75){$+U_{CC}$}
\put(9,75){$\pm U_r$}
\end{picture}
\end{center}
\caption{\it Circuit diagram.}
\label{fig:agic1}
\end{figure}

```

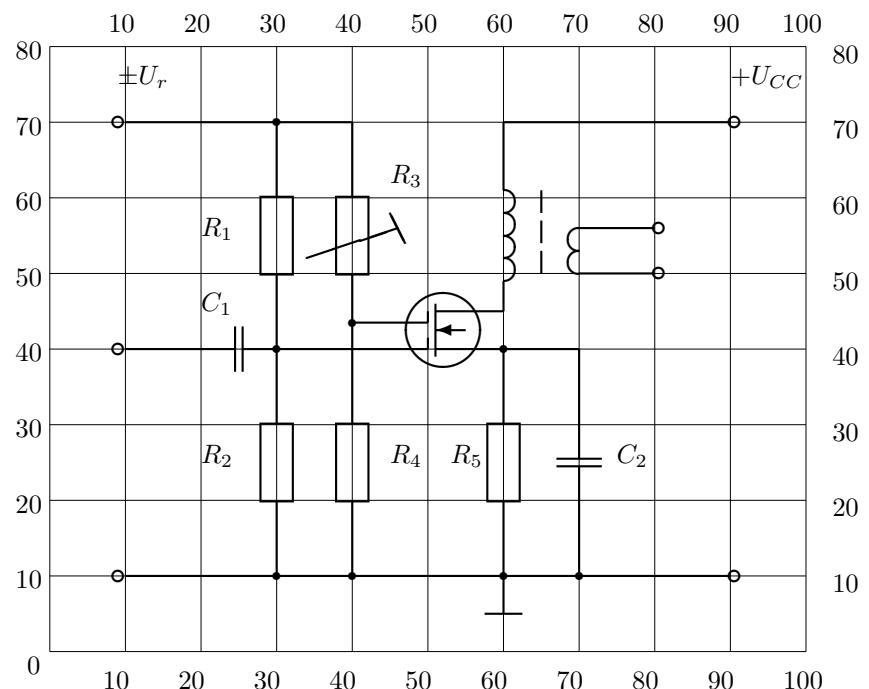


Figure 1: *Circuit diagram.*